

<b>Notice of References Cited</b>	Application/Control No. 09/212,915	Applicant(s)/Patent Under Reexamination TAKASU, HIDEKI	
	Examiner Michelle Estrada	Art Unit 2823	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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	N	JP-63261833-A	10-1988	Japan	Tsutomu	--
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., et al., Silicon Processing for the VLSI Era: Vol. 1, Process Technology, 1986 by Lattice Press, p. 133-136, 303-308 and 321-322.
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	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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